

## 4K cart

## 8K cart

✓ (A)  $\emptyset$  at <sup>4K</sup> card edge (always)

(A)  $\emptyset$  at <sup>4K</sup> card edge (first half Rom) <sup>requesting</sup>

+  
 $\emptyset$  at override switch  
=

+  
 $\emptyset$  at override switch  
=

no change

<sup>4K</sup> cart placed at 0K boundary  
(first 1/2 of 8K space) ✓

no change

8K cart, using first half of memory  
so address for 4K bank is at  $\emptyset$   
(no override function) ✓

intended change:  
use upper half

(B)  $\emptyset$  at <sup>4K</sup> card edge (always)

(C) 1 at <sup>4K</sup> card edge (second half) <sup>requesting</sup>

+  
1 at override switch  
=

+  
 $\emptyset$  at override switch  
=

<sup>4K</sup> cart placed at +4K boundary  
(second 1/2 of 8K space) ✓

no change

1 at address line, for 4K bank

✓ (C) 1 at <sup>4K</sup> card edge =  
impossible on 4K cart.  
("out of range" program.)

(B)  $\emptyset$  at 4K card edge  
+  
1 at override switch  
=

no-harm error

+  
 $\emptyset$  at ~~override~~ override  
=  
impossible-to-happen error

error

error: misaligned memory banks  
harm done, won't work.  
thinks its a 4K cart.  
cannot access lower half of Rom chip

✓ (D) 1 at <sup>4K</sup> card edge =  
impossible, so

(P) 1 at <sup>4K</sup> card edge  
+

1 at override =  
impossible +  
irrelevant

no change

1 at override switch  
=

reason no change happened  
(error, but harmless  
at this point.)